BEFORE PROCEEDING WITH COMPLETE UNPACKING AND SETUP, CONSULT UNPACKING AND INSPECTION INSTRUCTIONS

model 1176LN

PEAK LIMITER

EFFECTIVE WITH SERIAL NUMBER 7652

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SECTION I
INTRODUCTION

FIGURE 1-1. MODEL 1176LN FRONT PANEL.

1.1 DESCRIPTION

The Model 1176LN Peak Limiter is the most widely used device of its kind. Since its introduction in the late '60's, thousands have been used for applications which require precise, automatic control of peak signal levels in recording studios, disc mastering facilities, broadcast stations, sound reinforcement installations, etc. Continuing evolutionary developments have substantially changed the inner workings of the 1176 series, but the same excellent performance is common to all units, starting with number 1.

In the 1176LN, peak limiting is accomplished by utilizing an FET as a voltage variable resistor. A unique circuit permits severe limiting without added distortion, and the compression ratio may be optimized for various program materials.

Attack time is adjustable from less than 20 microseconds to 800 microseconds. The fast attack time is independent of program peak frequency or duration. Using the tone burst method, a 50 kHz peak is fully stabilized at the limited level within 1 cycle. The release time is also continuously adjustable on the front panel from 50 milliseconds to 1.1 seconds.

The gain structure throughout the limiter also maintains an excellent signal-to-noise ratio at normally used control settings. The front panel meter may be switched to read either the amount of gain reduction or the output signal level referenced to +4 or +8 dBm.

Provision is made to interconnect two 1176LN Limiters for those applications where two channels of program material are processed, and the balance of these audio channels must be maintained. The signal with the larger peak-to-peak amplitude will cause the same amount of gain reduction in both limiters. For this mode of operation, the accessory Model 1176SA Stereo Adapter is required.
1.2 SPECIFICATIONS

ELECTRICAL:

INPUT: Balanced bridging, differential amplifier.

INPUT IMPEDANCE: 20 kohms, used as balanced input.
10 kohms, used as unbalanced (single-ended) input.

MAXIMUM INPUT LEVEL: +20 dB maximum (Ref. 0.775 volts rms).

MAXIMUM GAIN: 45 dB, ±1 dB.

FREQUENCY RESPONSE: ±1 dB, 20-20,000 Hz.

OUTPUT: Floating, transformer isolated.

OUTPUT LOAD: 150 ohms or greater.

MAXIMUM OUTPUT LEVEL: +24 dBm into 600 ohm load (12.28 volts).

DISTORTION: Less than 0.5% THD, 50 Hz to 15 kHz with limiting, at 1.1 seconds release setting;
as with all limiting devices, distortion of low frequency peaks increases with faster release time.

SIGNAL TO NOISE RATIO: Greater than 81 dB at threshold of limiting, 30 Hz to 15 kHz.

ATTACK TIME: Less than 20 microseconds for 100% recovery; adjustable to 800 microseconds with front-panel control.

RELEASE TIME: 50 milliseconds minimum, 1.1 seconds maximum (for 63% recovery). Adjustable with front-panel control.

THRESHOLD vs OUTPUT LEVEL: Compressed

<table>
<thead>
<tr>
<th>Setting</th>
<th>Ratio</th>
<th>Compression Input Level at</th>
<th>Minimum Limiting</th>
<th>Threshold ±2 dB</th>
<th>Relative Output at Threshold*</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>20:1</td>
<td>-24 dB</td>
<td>Threshold ±2 dB</td>
<td>+10 dBm</td>
<td></td>
</tr>
<tr>
<td></td>
<td>12:1</td>
<td>-25 dB</td>
<td></td>
<td>+9 dBm</td>
<td></td>
</tr>
<tr>
<td></td>
<td>8:1</td>
<td>-26 dB</td>
<td></td>
<td>+8 dBm</td>
<td></td>
</tr>
<tr>
<td></td>
<td>4:1</td>
<td>-30 dB</td>
<td></td>
<td>+7 dBm</td>
<td></td>
</tr>
</tbody>
</table>

*with output gain control set to provide a reserve of approximately 10 dB.

NOTE: Throughout this manual, where the expression "dB" is used to denote a signal level, it is referenced to 0 dB = 0.775 V rms.
POWER REQUIREMENTS: 100 - 125 VAC, or 200 - 250 VAC, 50/60 Hz, switch selectable, less than 10 W.

ENVIRONMENT: Operating, 0°C to +50°C; Storage, -20°C to +60°C.

1.3 PHYSICAL

DIMENSIONS: 483 x 89 mm rack panel. Depth behind panel 203 mm (19" x 3-1/2" x 8").

FINISH: Panel is 3.18 mm (1/8") brushed clear anodized aluminum in 2 shades. Chassis is cadmium plated steel.

WEIGHT: 5 kg (11 lb).

SHIPPING WEIGHT: 6.6 kg (14.5 lb).

ACCESSORIES: Model 1176SA Stereo Adapter.
Model 301 XLR/QG Adapter for signal input and output.
Model SC-2 Security Cover.

1.4 FRONT PANEL CONTROLS

INPUT AND OUTPUT LEVEL: Continuously adjustable.

ATTACK TIME: Continuously adjustable, 20 microseconds to 800 microseconds. (Fastest attack is in the maximum clockwise (CW) position.)

LIMITING ON/OFF: This switch is coupled with the Attack Time control. In the full counterclockwise position (CCW) the limiting function is disabled.

RELEASE: Continuously adjustable from 50 milliseconds to 1.1 seconds. (Fastest release is obtained in the maximum CW position.)

COMPRESSION RATIO: Switch selectable with four interlocking pushbuttons.

METER FUNCTION: Three interlocked pushbuttons switch the limiter ON and switch the meter to read either the amount of gain reduction or the output signal level referenced to +4 dBm or +8 dBm. The fourth pushbutton switches the AC the power OFF.
GAIN REDUCTION
"0" ADJUST: Screwdriver adjustment accessible through a hole in the front panel (between the input and output level controls).

POWER SWITCH: Any of the meter function buttons switch the 1176LN ON. The device is switched OFF by depressing the red power switch button.

1.5 CONNECTIONS: Rear chassis barrier strip for input and output. Power through 3 wire IEC-style connector.

STEREO INTERCONNECTION: Phono jack at rear of chassis. Requires 1176SA Stereo Adapter to couple 2 limiters.

(See also Installation Instructions, Section 2.5, Figures 2-1 and 2-2.)
SECTION II
INSPECTION AND INSTALLATION

2.1 UNPACKING AND INSPECTION

Your Model 1176LN was carefully packed at the factory, and the container was designed to protect the unit from rough handling. Nevertheless, we recommend careful examination of the shipping carton and its contents for any sign of physical damage which could have occurred in transit.

If damage is evident, do not destroy any of the packing material or the carton, and immediately notify the carrier of a possible claim for damage. Shipping claims must be made by the consignee.

The shipment should include:

- Model 1176LN Peak Limiter
- UREI Instruction Manual (this book)
- Two-part Warranty Card bearing the same serial number as the Model 1176LN.
- Rack mounting hardware.

2.2 ENVIRONMENTAL CONSIDERATIONS

The system will operate satisfactorily over a range of ambient temperatures from 0°C to +50°C (+32°F to 122°F), and up to 80% non-condensing relative humidity.

If the system is installed in an equipment rack with high heat producing equipment (such as power amplifiers), adequate ventilation should be provided in order to assure longest component life. Also, while circuitry susceptible to hum pick-up is sufficiently shielded from moderate electromagnetic fields, installation should be planned to avoid mounting the system immediately adjacent to large power transformers, motors, etc.

2.3 POWERING

The 1176LN may be operated from either 100 - 125 VAC or 200 - 250 VAC mains (50 Hz or 60 Hz, single phase.) As indicated in Section 2.4, the nominal line voltage may be selected with a rear panel switch. BE SURE TO VERIFY BOTH THE ACTUAL LINE VOLTAGE, AND THE SETTING OF THE VOLTAGE SELECTOR SWITCH BEFORE CONNECTING THE 1176LN TO THE MAINS.
To comply with most Electrical Codes, the 1176LN is supplied with a three-wire IEC style connector, the grounding pin of which is connected to the chassis. In some installations this may create ground-loop problems. Ground loops can result in hum and buzz if a significant potential difference exists between the AC conduit ground and the grounded metal enclosure in which the chassis is installed. If hum is experienced, one may check for the possibility of ground loops by using a 3-prong to 2-prong AC adapter between the power cord and the mains supply, ungrounding the AC plug temporarily. This ungrounds the Model 1176LN, and may cure the hum or buzz, but is not a substitute for proper system grounding. Be aware that unless the Model 1176LN Peak Limiter is AC grounded, a safety hazard can exist. UREI accepts no responsibility for legal actions or for direct, incidental or consequential damages that may result from violation of any electrical codes.

2.4 LINE VOLTAGE SWITCH

Unless a tag on the line cord specifies otherwise, the Model 1176LN was shipped ready for operation with nominal 115 VAC power mains. In order to change this for nominal 230 V (50 Hz or 60 Hz), slide the VOLTAGE SELECTOR switch on the rear panel to the 230 position. The voltage is visible in a window next to the switch slot. Be sure to change the fuse to the correct value: 1/8-amp slo-blo when changing to 230 V operation or 1/4-amp slo-blo for 115 V operation. A small screwdriver should be used to move the recessed switch.

2.5 EXTERNAL CONNECTIONS

Permanent input and output signal wires should be shielded cable, and connected in accordance with standard wiring practice, as indicated on the rear panel barrier strip. If the XLR/QG connectors are to be used, install the accessory Model 301 according to the instructions supplied with the adapter. (See also "Mounting Instructions," Section VI Figure 6-1).

If the Model 1176LN output is connected to a high impedance circuit, we recommend shunting the "i" and "COM" output terminals with a 620 ohm, 1/2 watt resistor. This assures optimum loading.

(See Figures 2-1 and 2-2 on the following page for recommended connection procedures; and Section 2.6 regarding input termination.)
FIGURE 2-1. CONNECTING THE MODEL 1176LN WITH BALANCED INPUT AND BALANCED OUTPUT CIRCUITS.*

FIGURE 2-2. CONNECTING THE MODEL 1176LN WITH UNBALANCED INPUT AND UNBALANCED OUTPUT CIRCUITS.*

*With a balanced input and unbalanced output, or vice-versa, use the appropriate connections suggested by each of the above diagrams. No special switching or transformers are needed.
2.6 IMPEDANCE AND TERMINATION

Audio engineering had its roots in the telephone industry, and "600 ohm circuits" (together with their predecessors, "500 ohm circuits") are carry-overs from telephone transmission practices. Long audio transmission lines, like their video counterparts, must be properly sourced from and terminated in equipment which matches their characteristic impedance, if optimum frequency response and noise rejection are to be achieved.

However, transmission line theory and techniques are not only unnecessary but impractical within modern recording studios, broadcast studios and other local audio systems where transmission circuits are seldom more than several hundred feet in length. The advent of negative feedback circuitry and solid-state electronics has spawned modern audio amplifiers and other signal processing devices having source impedances of only a few ohms. They are essentially indifferent to load impedances and by varying their output current inversely to changes in load impedance, maintain the same output voltage into any load impedance above a rated minimum, with no change in frequency response. Most new designs (all at UREI) have high input impedance to allow use with various source impedances.

Most modern audio systems, therefore, utilize amplifiers and other active devices which have very low output impedances and high (10K to 50K) input impedances. These products may thus be cascaded (operated in tandem), or many inputs may be connected to a single output of a preceding device, without regard to impedance "matching". Switching, patching, etc. is simplified because "double loads" and "unterminated" bugaboos are essentially eliminated. "Floating" (ungrounded) transformer outputs minimize ground loop problems, and differential transformerless input circuitry (or input transformers) minimize common mode noise or interference which may be induced into the interconnecting wires or cables, or produced by different ground potentials.

Where audio must be transmitted through cables or wire pairs of more than several hundred feet in length, however, transmission line termination practices should still be observed.

The Model 1176LN has an input impedances of 20,000 ohms when used in a balanced, differential input configuration, and 10,000 ohms when used unbalanced (one side grounded). This makes the device suitable for use with any normal source impedance, low or high. Only when it is used from a source which requires a low impedance termination (such as a 600-ohm transmission line or older vacuum tube equipment) is a source termination resistor required at the 1176LN input.
2.7 ACCESSORIES

2.7.1 MODEL 301 ADAPTER

Two holes are provided adjacent to the ends of the IN/OUT barrier strip. This allows simple mounting of the Model 301 and adapts the limiter to be connected to signal cables fitted with XLR/QG termination.

2.7.2 STEREO ADAPTER 1176SA

The 1176SA Stereo Adapter is used to interconnect two 1176LN Limiters. The adapter is provided with an adhesive backing which allows it to be mounted on the rear chassis of the limiter or a nearby surface. Two cables are supplied with the 1176SA and are connected between the adapter and the two limiters. For calibration, see Section III, Operating Instructions, ¶ 3-6.

2.7.3 SECURITY COVER SC-2

An optional Security Cover is available to cover all operating controls of the Model 1176LN, and to protect against any inadvertent misadjustment of critical settings.
3.1 GENERAL DEFINITIONS

Before operating the 1176LN Peak Limiter with program material, it may be helpful to become familiar with the terminology used in this manual.

Compressors and Limiters are typically used in applications where the dynamic range of program material is too large to be processed by succeeding equipment, or where the peak-to-peak amplitude is too large for the headroom of the following equipment.

The two different names generally refer to the degree to which the dynamic range is restricted. The relationship of input level change versus output level change is called the Compression Ratio. If, for example, an increase of 8 dB input signal level should cause the output to increase by 2 dB, this would represent a 4:1 compression ratio.

Although no strict standards exist, amplifiers with compression ratios of up to 8:1 are typically considered to be Compressors, while those with ratios higher than 8:1 are called Limiters. By this definition, the following graph shows that the Model 1176LN can function as a compressor or a limiter since it has compression ratios selectable from 4:1 to 20:1.

![Graph showing transfer characteristics of the 1176LN Peak Limiter.](image)

**FIGURE 3-1. MODEL 1176LN TRANSFER CHARACTERISTICS.**
The transfer characteristic graph (Figure 3-1) also indicates the region of the Threshold. Signals at levels below the threshold will not be affected by the compression/limiting action. Higher signal levels will cause a controlled decrease in amplifier gain resulting in a change of dynamic range. The 1176LN Input Level control adjusts the amount of signal to be processed above the threshold, and hence the degree of compression or limiting.

The Attack Time of the 1176LN is the time it takes for the device to respond to a signal which exceeds the threshold. It is variable, and since the adjustment may have a significant effect on the sound quality of the program material, a more detailed description is given in the section on application.

The Release Time may be defined as the time for the limiter to return to its normal gain, after the signal which caused the gain reduction has dropped below threshold. The release time is variable and can also alter the sound of the program material considerably. The adjustments are discussed in more detail in the section on application.

3.2 INITIAL SET-UP

After the Model 1176LN has been installed and is connected to both the signal source and the succeeding equipment according to Section II, power may be turned ON by pushing any of the meter function switches. Set the controls as follows:

- **Input**: CCW
- **Output**: Half rotation
- **Attack**: Fully CCW, switch to OFF position
- **Release**: Half rotation
- **Compression Ratio**: 8:1
- **Meter Function**: +4 dB

3.3 PERFORMANCE CHECK

The following steps will verify the general performance of the limiter and indicate the operation of all functions. If the unit fails to pass these tests, malfunction is indicated.

3.3.1 INPUT-OUTPUT SIGNAL LEVEL

Apply a sine wave signal with a level of approximately 0 dB (0.775 V rms) to the input terminal. Turn the Input control CW to approximately half-rotation. There should be an indication on the VU meter. Adjust the Output control until the VU meter reads "0". Measured with an external meter, the output signal will read about +4 dBm.

Push the +8 dB meter function switch. The VU indication will drop by +4 dB. The actual output level will not change.
3.3.2 METER ZERO ADJUST (Allow 15 minutes warmup)

Push the "GR" meter function switch. The VU meter should read "0" since the limiting function is disabled (the Attack control knob is turned OFF). If the "0" indication has drifted beyond ±1 dB, it should be adjusted. Using a small screwdriver, this may be done through the hole in the front panel located between the Input and Output level controls.

3.3.3 GAIN REDUCTION CHECK

Turn the Attack control ON; gain reduction should be indicated. Observe the tracking between the indicated amount of gain reduction and the actual change in output (VU) level, as measured with an external meter. The tracking should be within ±1 dB.

Selecting other compression ratios of the limiter will indicate some gain reduction in each setting, however the actual amount will vary due to the different threshold sensitivities.

3.4 APPLICATION

3.4.1 CONTROL SETTINGS

Since all important parameters of the Model 1176LN are variable, the instrument is flexible enough to be used in virtually all phases of sound processing, from recording of a single track to reproduction of the full program material. The limiter finds application as a creative instrument during production, as well as a device for automatic level control and protection against high amplitude problems.

There is no single correct setting of the controls; rather experimentation is necessary to find the best combination of compression ratio, attack and release time, and the amount of gain reduction for the job at hand. Experience will show that the dynamic parameters (attack and release time) have a stronger effect on the character of the program material than the static parameters (compression ratio and threshold).

3.4.2 EFFECTS OF COMPRESSION RATIO

A moderate form of gain reduction is achieved with ratios up to about 8:1. The dynamic range of the program material is controlled without obvious alteration. The average signal level will be increased and the softer passages will be made louder.

A more drastic gain reduction results from compression ratios of 12:1 and 20:1. The output signal level is limited, and for practical purposes cannot exceed a preset level. Dynamic range is significantly reduced, and high level peaks are minimized.
3.4.3 EFFECTS OF THRESHOLD

Changing the relationship of the average input level to the
threshold will determine the amount of gain reduction, or how much
of the signal envelope is affected by the limiter action.

If only occasional large-amplitude peaks are allowed to exceed
this threshold, the program material remains very much the same.
However, overload problems which would be caused by these peaks are
eliminated. The result can be an increase in loudness because the
operator can raise the signal level without adding distortion due to
headroom limitations.

When the input is adjusted so that the average signal level is
above the threshold all the time, then the entire program material
will be compressed. Monitoring the amount of gain reduction is
made easy when the meter function is switched to the "GR" mode.

3.4.4 EFFECTS OF ATTACK TIME

Fast transients and high frequencies with large peak-to-peak
amplitudes can only be limited or compressed if a fast attack time
is chosen. Then any overshoot and, with it, overload problems, are
kept to a minimum. However, limiting with a fast attack time alters
the musical quality of some instruments which are characterized by
sharp percussive attacks.

A slow attack time will allow the first segment of fast ampli-
tude changes to pass through the limiter unaffected. The limiter
reduces gain after the initial envelope attack of the program
material. Thus, slow attack maintains the "punch" of such percus-
sive instruments as drum, bass, piano, guitar, etc. with some
sacrifice of amplitude control.

The aim of the user in adjusting the limiter for the correct
attack time then is to find the best setting for the task at hand.
It may be a compromise between a permissible amount of initial peak-
to-peak amplitude before full compression, and how much alteration
of the program material is acceptable.

These general guidelines are mainly applicable when the 1176LN
is used for the limiting of individual signal sources. For mixed
program material, a middle-of-the-range setting will usually be a
good starting point for additional experimentation.

3.4.5 EFFECTS OF RELEASE TIME

This control, too, may be set to accommodate different program
material requirements. The best adjustment maintains a quick
response of the limiter to dynamic changes of the signal while
avoiding such affects as "pumping" or "breathing." Again, let us
examine the behavior with some extreme settings.
If the Release Time is set very short, the limiter will return the gain reduction to zero every time the signal drops below the threshold. For low frequency solo passages, such as bass and drum, this can occur between cycles or individual notes and beats. We would hear the gain come up each time, and, with it, the softer passages or background noise. Extremely short release times may even introduce harmonic distortion if the gain reduction changes between cycles.

Setting the Release Time to a very long duration will maintain the initial gain reduction caused by a large amplitude, and the dynamic range of the passage which follows is strongly affected. If the gain reduction continues through a soft program section, it may make this section inaudible because it, too, will be reduced in gain. Also, short dynamic changes will be ignored due to the continuous high level of gain reduction.

The most desirable Release Time setting for particular program material can usually be found quickly by experimentation. Here, too, the effects of varying the adjustment are more obvious with individual signals, and less apparent with a full mix. As with the Attack control, a good starting point is half rotation.

3.5 PLACEMENT OF THE 1176LN LIMITER IN THE SIGNAL PATH

3.5.1 AFTER PREAMP

Small signals need to be amplified before they will cause proper operation of the 1176LN Limiter. Only signal levels which are above the threshold of the limiter can cause gain reduction. For example: In the 4:1 compression ratio, the lowest threshold is -30 dB. If the signal applied to the input is about -20 dB, then the 1176LN will provide a maximum of 10 dB compression with the Input Level control set to maximum. The sensitivity is high enough that it should not be difficult to obtain sufficient signal levels from most sources which supply the audio to be processed by the 1176LN.

This consideration is appropriate when the limiter is patched into the signal path at a point where individual inputs are to be controlled artistically before mixing (solo tracks).

3.5.2 EQUALIZATION, BEFORE OR AFTER THE LIMITER?

In most cases, better results are obtained if one places the limiter after the equalizer. This becomes evident when one considers that the equalizer could eliminate the amplitude balance so carefully achieved with the limiter. An exception to this would be in the case of large dynamic changes in program material that are too difficult for the equalizer to handle, and that would thus result in overload or a poor signal-to-noise ratio. Then the 1176LN should be used to condition the audio signal before it is applied to the equalizer or other device.

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3.5.3 FULL PROGRAM MATERIAL

It is good to recall that the action of a limiter is most obvious when applied on the full program material because at this point one does not benefit from the masking that occurs when individually limited sources are mixed. Therefore, it is even more important to carefully select the dynamic parameters, like attack and release time, to achieve an optimum result. Here are some additional tips:

On live program material, the fastest attack time possible will afford the maximum protection against overload, but will cause some alteration of the sound. On pre-recorded tape, disc or film, slower attack time may be used to maintain a high average level.

For program material containing relatively little low frequency energy, the release time may be fast, thereby increasing the short interval average level. The highest apparent average level may be attained with the shortest release time before "pumping" becomes objectionable. For program material containing more than average low frequency energy, the release time must be increased to the point that no low frequency distortion is apparent. A further compromise may be made by slightly reducing the amount of limiting action in favor of slightly faster release time: the result -- a higher average level.

Adjustment of the Attack Time control also affects the amount of sibilant speech sound. These high frequency components in speech are normally at much lower level than the low frequency components, and the control voltage derived from sibilants alone is much less than from the "vowel" or low frequency sounds. In cases where attack time is extremely slow, the rise time and amplitude of the high frequency sibilants will not cause control voltage to be developed until the "vowel" energy envelope occurs. This results in the full gain applied to the sibilants and reduced gain to the vowels, creating an exaggerated sibilant sound. Again, experimentation is the best tool.

3.5.4 PRIOR TO OUTPUT AMPLIFIER

Here the limiter fulfills mainly the function of protection. Most equipment which is fed from the low level signal path's output terminal is sensitive to amplitude overload; the power amp's peak-to-peak headroom, the saturation point of recording tape, the modulation allowed in a transmitter, the amplitude handling capability of film recording devices, etc.
If one were to summarize the ideal operation of a well adjusted limiter, one could say:

"It should afford infallible protection against overload, while maintaining the highest level, contribute no apparent degradation to the dynamic range which might subjectively affect the artistic value of the program, and, at the same time, maintain the individual quality of all sounds, regardless of their starting transients."  

3.6 STEREO INTERCONNECTION OF TWO 1176LN UNITS

To calibrate the accessory 1176SA Stereo Adapter, first remove signals from both limiters, and disable gain reduction by rotating the Attack controls fully CCW. Connect the 1176SA to both limiters. Set the meter function switches for "GR" mode. Adjust the Meter Zero adjustment on the 1176SA until both meters read zero. If the meters cannot be zeroed, reverse the stereo interconnect cables, and the meters can then be zeroed.

The same compression ratio should be selected in both units. Also, the limiters should be set up separately for threshold and output level before being linked together for stereo operation. When two 1176LN limiters are interconnected, the Attack controls on both units will interact, as will the Release controls. Since the timing capacitors are in parallel, the fastest attack time will be double that of a single unit. Attack time on either limiter can be separately adjusted to control both units. Maximum release time is the same as that of a single unit. A good procedure would be to set the Release control on one 1176LN to maximum, and use the Release control on the other unit to control release time.

A given pair of 1176LN's will normally track properly through at least 10 dB of gain reduction. In some instances, transconductance of the two VVR FET's (Q1) will differ to the extent that equal gain reduction will not be obtained as limiting is increased. This condition is more apt to be present in 1176LN's with widely different serial numbers, as FET's within the same factory production run are normally quite well matched. Should this anomaly be observed, it will be necessary to select FET's for Q1 which match more closely in transconductance. This is better done at the factory.

SECTION IV
THEORY OF OPERATION

4.1 INPUT AMPLIFIER

The audio signal is applied through the rear panel barrier strip to a differential input amplifier (IC 2, Sections A & B). This input can accept either balanced or unbalanced signal sources. When used in an unbalanced mode, either the COM terminal or the "+" terminal is connected to ground, the latter causing a polarity reversal (180° out of phase) of input versus output signal.

Common mode rejection is typically better than 60 dB.

4.2 VOLTAGE-VARIABLE RESISTOR ATENUATOR

The input signal is coupled from the differential amplifier through the Input Level control to an "L" section consisting of R6 as the series element and field-effect transistor Q1 as the voltage-variable shunt element (VVR). Below the threshold of limiting, the VVR has a high resistance due to its quiescent bias. Starting at the threshold point, the bias is reduced, causing the VVR resistance to decrease. The rate of change of bias, as well as the threshold point, is controlled by the Compression Ratio pushbutton switches.

4.3 SIGNAL PREAMPLIFIER

Transistors Q2, Q3 and Q4 comprise a low-noise preamplifier. A large amount of overall negative feedback results in low distortion and more than adequate drive capability. The preamplifier output signal is applied simultaneously to the Output Level control and to a signal voltage divider for use in the gain reduction control amplifier.

4.4 LINE AMPLIFIER

The signal from the output control is coupled to the output amplifier consisting of Q5 through Q9 and associated components, and hence to the output transformer T2. This circuit uses a special transformer designed by UREI for low phase shift, flat response, and excellent overload characteristics.

4.5 GAIN REDUCTION CONTROL AMPLIFIER

This amplifier receives its input signal from a voltage divider at the output of the preamplifier. The signal from the divider is selected by the Compression Ratio pushbutton switches. Transistors Q12 and Q13 make up a phase inverter and emitter-follower. The
output of Q13 is supplied to a rectifier diode CR4 and to another phase-inverter emitter-follower combination Q14 and Q15. The output of Q15 is supplied to rectifier diode CR3. Since the two signals are out-of-phase, CR3 and CR4 full wave rectify the signals. When filtered by C27, this produces a dc voltage proportional to the signal amplitude. This is a positive-going voltage which subtracts from the bias on the VVR. To create a threshold of limiting, diodes CR3 and CR4 are biased by a dc voltage divider which is ganged with the Compression Ratio pushbutton switches.

4.6 GR METER DRIVER CIRCUIT

Gain reduction is indicated on the front panel VU meter by measuring the bias on the VVR. Field-effect transistor Q11 provides an impedance transformation from the high impedance VVR bias line to the relatively low input impedance of operational amplifier IC 1. Zero gain reduction is indicated by a quiescent current through the VU meter. When limiting occurs, and the VVR bias is reduced, the current in the VU meter is reduced by an amount corresponding to the amount of gain reduction, and the needle deflects downscale.

4.7 POWER SUPPLY

Two independently regulated voltages are supplied to the circuitry of the 1176LN, +30 VDC and -10 VDC. The AC mains voltage is connected through the front panel switch assembly which also selects the meter function. The 1176LN is switched ON when any of the meter function buttons is depressed. The positive voltage is regulated through VR1. The negative voltage is regulated with a 10 volt zener diode, CR6.
SECTION V
MAINTENANCE

5.1 GENERAL

The Model 1176LN is an all solid-state unit, ruggedly constructed with only the highest quality components. As such, it should provide years of trouble free use with normal care. All parts used are conservatively rated for their application, and workmanship meets the rigid standards you have learned to expect in UREI products.

NO SPECIAL PREVENTIVE MAINTENANCE IS REQUIRED.

5.2 REPAIRS AND WARRANTY

This product is factory warranted to the original purchaser against defects in material and workmanship for one year after initial purchase. This limited warranty must be activated at the time of purchase by returning the registry portion of the Warranty Card to the factory. Should a malfunction ever occur, the dealer from whom the unit was purchased will be glad to handle return for factory repair. Please call or write to the factory for a Return Authorization Number which must accompany all repairs. For prompt service, ship the unit prepaid directly to the factory with the RA Number visible on the shipping label. Be sure it is well packed in a sturdy carton, with shock-absorbing material such as foam rubber, styrofoam pellets, or "bubble-pack" completely filling the remaining space. Particular attention should be paid to protecting the controls, switches, etc. Tape a note to the top of the unit describing the malfunction, and instructions for return. We will pay one-way return shipping costs on any in-warranty repair.

Because of specially selected components in this product, field repairs are not authorized during the warranty period, and attempts to perform repairs may invalidate the warranty.

Even if your unit is out of warranty, we recommend that you return it to the factory for repairs. Our experienced personnel, supported by special test equipment, will be able to find and eliminate any problem in the most efficient way.

5.3 INTERNAL SERVICE ADJUSTMENTS

These controls have been set at the factory and should not require adjustments except after service work. If recalibration is necessary, the test procedure that follows should be performed very carefully, and adjustments performed in the exact manner and order specified.

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Before attempting any calibrations, the limiter should be operated for approximately 15 minutes. This avoids subsequent drifting.

**WARNING:** The full AC line voltage is present at several points inside the chassis. Be careful to avoid personal shock when you work on the limiter with the covers removed.

5.3.1 POWER SUPPLY

The positive DC voltage should be +30 volts (±0.5 volts). The test point to measure the level is at the collector (metal tab) of Q8.

The level of the negative voltage supply is a fixed -10 volts (±0.5 volts), and cannot be adjusted. The test point is the metal case of IC 1.

5.3.2 "Q" BIAS ADJUSTMENT

This is a very important parameter to assure the linear operation of the limiter. Therefore, the adjustment should be performed very carefully. Set the controls as follows:

- **Input** = full CCW
- **Output** = full CW
- **Attack** = full CCW (switched to OFF position)
- **Release** = full CW
- **Compression Ratio** = 20:1
- **Meter Mode** = +4 dB
- **Q-Bias Adjust** = full CCW
  (R81, internal trimpot)

Apply a signal (1 kHz, 0 dB) to the input, and turn the Input control CW until the VU meter reads +1 VU. Slowly turn the Q-Bias Adjust (R81) CW until a drop of 1 dB occurs, and the meter reads 0 VU. This places the gain reduction FET Q1 slightly into conduction.

5.3.3. GAIN REDUCTION METER TRACKING

Due to interaction of the adjustments, this procedure may have to be repeated to achieve satisfactory tracking. Set the controls as follows:

- **Input** = mid rotation
- **Output** = full CW
- **Attack** = full CCW (switched to OFF position)
- **Release** = full CW
- **Compression Ratio** = 20:1
- **Meter Mode** = "GR"
- **R54** = 1/4 turn from full CCW
  (internal trimpot)
With no input signal applied, adjust the GR meter (R55) to read "0" VU. This adjustment is accessible through a small hole in the front panel between the Input and Output controls.

Apply a signal (1 kHz, -10 dB), switch the meter mode to +4 dB, and turn the Output Level control CW until the meter reads "0" VU. Turn the Attack control ON (CW) and observe the drop in the meter reading. Adjust the Input Level control until -10 dB is indicated on the VU meter. Turn the Attack control OFF (CCW) and readjust the Output Level control for "0" VU, if necessary. Repeat these last two steps until the output drops 10 dB whenever the Attack Control is turned ON.

Now, without touching the Input or Output controls, select the "GR" meter mode and turn R54 (tracking adjustment) until the GR meter reads -10 when the Attack control is ON, and adjust R55 until the GR meter reads "0" VU when the Attack control is OFF. Due to interaction of these controls, the fastest technique to adjust for best tracking is to correct only one half the error with R54 before rechecking the "0" reading with the Attack control turned OFF. Several iterations will be necessary.

5.3.4 GR METER ZERO

This adjustment is accessible through a hole in the front panel (R55) and was adjusted during calibration of GR meter tracking in Section 5.3.3. However, it may be readjusted without significantly affecting the other control settings. A small amount of drift (±1 dB) is normal in this circuit.

5.3.5 SIGNAL PREAMP LINEARITY

This control (R16) is in the feedback loop of the amplifier and affects the operation of Q1. It will never be necessary to perform any adjustment of R16 unless resistors in this section of the circuit have been replaced. If adjustment is required, set the controls as follows:

\[
\begin{align*}
\text{Input} &= \text{full CW} \\
\text{Output} &= \text{to number "18" on the front panel} \\
\text{Attack} &= \text{full CCW (switched to OFF position)} \\
\text{Release} &= \text{full CW} \\
\text{Compression Ratio} &= 20:1 \\
\text{Meter Mode} &= \text{"GR"}
\end{align*}
\]

Apply an input signal (500 Hz, -30 dB) and measure THD of the resulting output signal. Adjust R16 until the minimum amount of distortion is achieved.

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5.4 IN CASE OF DIFFICULTY

5.4.1 GENERAL

The overall schematic, circuit description (Section IV), and troubleshooting table (Table 5-1) can often be used to isolate a problem. Safety considerations outlined in Section II apply when working inside the device. If a problem cannot easily be solved, it is best to send the unit to the factory using the procedure described under paragraph 5-2. Remember, our technicians have tested and aligned thousands of 1176LN's, and are most qualified to repair your instrument.

5.4.2 PRECAUTIONS FOR TROUBLESHOOTING

Be cautious when testing IC and transistor circuits. Although they have almost unlimited life when used properly, they are very vulnerable to damage when accidentally shorted or connected to incorrect voltages.

Be sure not to short any terminals when making measurements. If a probe should slip, for example, and short out a bias or supply point, it will very likely damage IC's, transistors or diodes. Do not remove any components while the line cord is connected to the AC outlet.

5.4.3 VISUAL TEST

Look at wiring and connections. Check to be sure that all transistors and IC's are properly fitted into the sockets. Check for resistors which may look burned, indicating trouble in associated circuitry.
<table>
<thead>
<tr>
<th>PROBLEM</th>
<th>POSSIBLE CAUSE</th>
<th>REMEDY</th>
</tr>
</thead>
</table>
| Signal does not pass through the device. | 1. Power supply defective.  
2. Device in full limiting.  
3. Loose or broken wires between PC board and front panel controls or barrier strips.  
4. Incorrect hookup.  
5. Bad amplifier section. | Check supply voltages.  
Remove Q1. If signal passes, go to problem of "full limiting".  
Isolate and resolder.  
See Section II, Installation.  
Repair. |
| No limiting. | 1. Limiting switched OFF.  
2. No compression ratio button is depressed.  
3. Input level is below threshold.  
4. "Q" Bias misadjusted.  
5. GR amplifier defective.  
6. Wire from Attack control to R7 is open. | Turn Attack control CW.  
Select desired ratio.  
Increase input signal level.  
Adjust (§ 5.3.2).  
Repair.  
Repair. |
| Always in full limiting. | 1. Shorted GR bus.  
2. Negative supply voltage.  
3. "Q" Bias misadjusted.  
4. Contaminated PC board. | Check wiring; check stereo interconnect; unplug 1176SA.  
Replace zener diode and realign.  
Adjust (§ 5.3.2).  
Clean (§ 5.5). |
| Hum through output. | 1. Ground Loop, etc.  
2. Failure to unbalance output transformer into unbalanced load. | See Section II, Installation.  
Ground "COM" to chassis. |

(Continued on next page)
<table>
<thead>
<tr>
<th>PROBLEM</th>
<th>POSSIBLE CAUSE</th>
<th>REMEDY</th>
</tr>
</thead>
<tbody>
<tr>
<td>(Hum through output)</td>
<td>3. Mounted too close to strong EMI field.</td>
<td>Relocate unit.</td>
</tr>
<tr>
<td></td>
<td>4. Power supply.</td>
<td>Check filter caps.</td>
</tr>
<tr>
<td>Distortion (during no limiting mode)</td>
<td>1. Power supply voltage.</td>
<td>Repair as needed.</td>
</tr>
<tr>
<td></td>
<td>2. Input level too high.</td>
<td>Adjust.</td>
</tr>
<tr>
<td>Distortion (during limiting)</td>
<td>1. &quot;Cracking&quot; sound on attack.</td>
<td>Attack set too fast or check Q-Bias (§5.3.2).</td>
</tr>
<tr>
<td></td>
<td>2. Low frequency distortion.</td>
<td>Select slower settings of Attack and/or Release controls.</td>
</tr>
<tr>
<td>Release control changes the output level.</td>
<td>1. Contaminated PC board.</td>
<td>Clean (§ 5.4.4).</td>
</tr>
<tr>
<td></td>
<td>3. Caps C19, C20 leaky.</td>
<td>Replace, 6.8 µf, 35V.</td>
</tr>
<tr>
<td>VU meter reads low on +4, +8 range.</td>
<td>Bad meter.</td>
<td>Replace.</td>
</tr>
<tr>
<td>GR meter zero control inoperative.</td>
<td>1. Bad connection from PC board.</td>
<td>Repair.</td>
</tr>
<tr>
<td></td>
<td>2. Dirty switch assembly.</td>
<td>Clean with good contact spray through back of switch.</td>
</tr>
<tr>
<td>Excess noise.</td>
<td>Problem in IC2, Q2,3,4,5.</td>
<td>Replace.</td>
</tr>
<tr>
<td>1176SA Stereo Adapter does not work.</td>
<td>Dead battery.</td>
<td>Replace, NEDA 15 M (1.4 V). Life is normally &gt;3 years.</td>
</tr>
</tbody>
</table>
5.5 PC BOARD CLEANING

Due to very high impedance circuits, it may happen that contamination on the PC board causes the limiter to perform poorly. Moisture, smoke or pollutants in the air may result in slightly conductive deposits which affect the operation of Q1 due to leakage. Existence of contamination can be verified with a simple test. Supply a steady input tone and, with no limiting indicated, adjust the Input and Output controls to read 0 VU on the meter. Remove the top and bottom covers from the limiter and locate Q1. From close proximity, exhale deeply on the circuit board around Q1. The moisture in the breath will induce surface leakage, and the VU meter will indicate a drop in output level. If no contamination is on the board, the output level will return very rapidly to 0 VU (within 10 seconds or less). If contamination is on the PC board, the moisture will be retained for a longer time and cleaning of the board is necessary.

 Disconnect the power cord from the AC mains. Prepare a cleaning solution by mixing equal parts of distilled water and pure isopropyl alcohol (not rubbing alcohol). Use a new tooth brush (or similar stiff bristle brush) and apply the mixture to the circuit board. Brush vigorously to thoroughly clean the top and bottom of the board, and allow the board to dry completely before applying power to the limiter. In extreme cases it may be necessary to replace the socket which holds Q1. Repeat the moisture test before replacing the top and bottom covers.

5.6 CLEANING THE LIMITER

The front panel of the 1176LN may be cleaned with a non-abrasive cleanser such as "Formula 409" or "Fantastic" applied with a soft clean cloth. Additional protection of the anodized panel can be afforded through a light application of a spray wax preparation such as "Pledge." Never spray the panel directly, as the cleanser or wax may adversely affect controls or meter, and can contaminate circuit boards if it penetrates the chassis.

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FIGURE 6-1. MOUNTING INSTRUCTIONS FOR MODEL 301.
1. Visual Inspection

Conditions: This inspection must be done before any tools or setup are applied to the product.

Test: Check to make sure the front panel and meter face are free from scratches, stains, discolorations, silkscreen errors, etc. See that all appearance parts are properly aligned and free from defects in their appearance and functional behavior. Also check to see that there is no loose hardware and that all workmanship standards for soldering have been met.

Inspect all the power supply electrolytic capacitors and diodes along with all the product’s i.c.'s to be installed with the correct polarity BEFORE turning the unit on. Also check the fuse value to be 250 volts, .25 amps, 3AG slow blow.

If any of the above requirements are not met send the unit to be reworked and notify the leadperson who delivered the products to the test department of the unit’s failure.

2. Power Supply

CAUTION: DO NOT lean over the product when the power is first applied if the top cover is off the unit. Be ready to quickly turn off the power if a destructive condition should manifest itself.

Conditions: No input signal and no output load on the unit.
Set the line voltage with an autotransformer to 120 Vac.

Test: Place a d.c. voltmeter probe on the input to the power supply regulator VR1 and then switch on the power to the unit. Measure the voltage to be according to the following table along with all other specified measurements and tolerances.

<table>
<thead>
<tr>
<th>Test Point</th>
<th>d.c. Voltage</th>
<th>Tolerance</th>
</tr>
</thead>
<tbody>
<tr>
<td>input of VR1</td>
<td>+40.5</td>
<td>+ or - 1.5 Volt</td>
</tr>
<tr>
<td>anode of CR6</td>
<td>-10.0</td>
<td>+ or - .5 Volt</td>
</tr>
<tr>
<td>output of VR1</td>
<td>+30.0</td>
<td>+ or - 1.0 Volt</td>
</tr>
<tr>
<td>* terminal of C15</td>
<td>+14.0</td>
<td>+ or - 1.0 Volt</td>
</tr>
</tbody>
</table>

Place a d.c. voltmeter probe on the input to the VR1 regulator and switch the line voltage switch, on the back of the unit, to the 230 position. Measure the voltage to be approximately +19.5 Vdc. Remember to set the line voltage switch back to the 115 position.

Vary the line voltage level with the autotransformer from 100 Vac to 140 Vac and measure the difference between these two voltage levels at the output of VR1 to be no more than 500 millivolts. Also, check to make sure that the red power LED is properly illuminated when the power is switched on.

Note: For all the following tests that do not involve limiting be sure to have the Attack pot switched off in the full counterclockwise position.
3. Power Consumption

Conditions: No input signal or load on the output.
Set the line voltage with an autotransformer to 120 Vac.

Test: Measure the input power of the unit at its line cord to be less
than 10 watts. If a power meter is not available then the average
power can be determined by measuring the r.m.s. voltage and the
r.m.s. current, and multiplying their product by the cosine of the
phase angle between them.

4. Gain

Conditions: No load on the output.
Input and output pots full clockwise.

Test: Apply a -25 dbu, 2 kHz input signal then measure the output level
to be within +1 or -1 dbu of +22 dbu.

Note: Units without their FET's will have this gain however units that
have been properly biased and have the limiting switched off will
have approximately 1 db less gain.

5. Common Mode Rejection

Conditions: No load on the output.
Input pot full clockwise.
Make sure the reference signal does not clip.

Test: Apply a -10 dbu, 2 kHz input signal and set an output reference
level with the output pot. Connect the signal to both the + and -
input terminals at the same time then adjust trimpot R2 for a
minimum output level. Measure the output level at 60 Hz and 20 kHz
to be more than 70 db below the 2 kHz reference level that was set
before tying the + and - together.

6. Input Impedence

Conditions: Use an a.c. voltmeter with an input impedance > 1 MOhm.
The shield and common wires on the input must not be
shorted to each other or to any ground.

Note: This test requires two 10 kOhm, 1% resistors in series with the
input of the 1176LN. One from the (+) output of the generator to the
(+) input terminal of the 1176LN, and another in series from the (-)
terminal of the generator to the (COM) input terminal of the 1176LN.

Test: With the 1176LN disconnected from the series resistors, place the a.c.
voltmeter on the output of the 10 kOhm source resistors and set the
generator for a reference level of 0 dbu at 2 kHz. Connect the input
of the 1176LN to the output side of the source resistors and see that
the level drops 6.0 db with + or - .25 db tolerance.
7. Output Impedence

Conditions: Square wave generator is needed for this test.
No load on the output.
Make sure the reference signals do not clip.

Test: Place an a.c. voltmeter directly across the + and - output terminals of the limiter and set a reference level of +20 dbu at 2 kHz using a sine wave. Connect a 600 Ohm resistor and see that the level drops .55 db with + or - .1 db tolerance.

Set the sine wave generator for an output reference level from the limiter to have a +10 dbu output at 2 kHz with no load. Connect a 150 Ohm resistor and see that the level drops 2 db with + or - .1 db tolerance.

Apply a 2 kHz square wave to the input of the limiter and check the output waveform on an oscilloscope to have no ringing on either the positive or negative tops after the leading and trailing edges. This should be done using no load and a 600 Ohm load on the output.

Note: The load resistors should have at least a .5 watt rating with a 1% tolerance on their values of resistance.

8. Frequency Response

Conditions: Make sure the reference signal does not clip.
600 Ohm load on the output.

Test: Apply a 2 kHz input signal and set an output reference level of 24 dbu. Sweep the signal generator from 20 Hz to 20 kHz and check to see that the output level changes no more than +.5 db and -1 db from the reference level.

Increase the frequency of the input signal until the output level drops to 3 db below the reference level then measure this frequency to be approximately 55 kHz with + or - 10% tolerance.

Note: Be sure to monitor the input signal level so as to insure that the reference input level does not vary. Some generators need to settle, especially at low frequencies, before a measurement can be made.

9. Maximum Input and Output Levels

Conditions: Input pot full clockwise.
150 Ohm load on the output.

Test: Apply a -10 dbu, 2 kHz input sine wave signal then adjust the output pot so that the signal just starts to clip. This level should be greater than 20 dbu.

Set the input pot so that the output level drops 40 db. Increase the input signal until the output waveform shows clipping then measure the input signal to be greater than +20 dbu (7.75 Vrms).
Conditions: Trimpot R81 full counterclockwise.
Attack pot full counterclockwise. (Switched off.)
Release pot full clockwise.
Ratio switch set to 20:1.

Test: Apply a -10 dbu, 2kHz input signal and adjust the input and output pots for a 10 dbu output reference level. Adjust R81 so that the output level drops exactly 1.0 db.

Note: If the FET (Q1) is installed in the 1176LN then it is necessary to remove it after setting a reference level in order to make sure that it is not conducting and thereby giving a false reference level. After it is determined that there is no change in level then the FET should be reinstalled for the calibration to be made.

11. Meter Calibration

Conditions: 600 Ohm load on the output.
Attack pot full counterclockwise. (Switched off.)
Release pot full clockwise.
Ratio switch set to 20:1.
Set the meter switch to the GR position.

Test: Before power is applied to the unit make sure the mechanical zero adjustment on the face of the meter is set on the mark just below the -20 Vu on the meter then apply a 2 kHz, -10 dbu input signal and set an output reference level of 10 dbu with the input and output pots. Set trimpot R55 (through the hole in the front panel) so that the meter on the 1176LN reads exactly 0 Vu.

Switch the attack pot full on and use the output pot to set an output level at 0 dbu then switch the attack pot full off and use the input pot to set an output reference of 10 dbu. Repeat this process until there is exactly a 10 db drop from the 10 dbu reference level when the attack switch is full on.

With the attack pot full on set the trimpot R54 for exactly a -10 db Vu on the meter on the 1176LN. Switch the attack off and reset 0 Vu with R55 then keep repeating this on/off calibration until it is no longer needed (usually 2 or 3 times).

Apply a 2 kHz, -10 dbu input signal and set an output reference level of 4 dbu with the output pot. Set the meter switch to the +4 position and see that the meter on the 1176LN reads 0 Vu within ± or - .5 db. Repeat this setup for an output level of 8 dbu and the meter switch set to the +8 position.

Check to make sure that the lamps inside the meter of the 1176LN have the proper illumination and that they are matched in brightness to each other. Also the needle in the meter should have smooth motion throughout it's range of operation.
12. Noise

Conditions: 600 Ohm load on the output.
600 Ohm terminator on the input terminal.
Input and Output pots full clockwise.

Test: Using a 30 Hz to 15.7 kHz bandpass pass filter on the input of an a.c. voltmeter measure the output level of the limiter to be less than -57 dbu. Rotate the input pot full counterclockwise and measure the level to be less than -63 dbu.

Use an oscilloscope that is triggered on the line frequency and set for 5 mSec/div to measure the output of the voltmeter. Look for a continuous white noise level that has no more than 1 db jumps in level. There should be no burst (popcorn) noise and maybe only small level spikes (60 and/or 120 Hz) induced from the power transformer.

Note: The bandpass filter that is specified in this test has slope rates for the high pass and low pass sections of 6 db/octave.

Also the specification sheet says that there must be greater than 81 db signal to noise ratio at at the threshold of limiting. This number is calculated by adding the limiter’s gain to the absolute value of the noise level and then subtracting the amount of the difference of 0 dbu and the threshold of limiting. Typically however, given the limit of -57 dbu and the threshold of limiting for the 4:1 ratio setting is specified at -30 dbu the s/n ratio will be 74 db. This improves at the 20:1 setting with the specified threshold at -24 dbu and therefore a s/n ratio of 80 db.

13. Ratios

Conditions: 600 Ohm load on the output.
Attack pot full counterclockwise. (Switched off.)
Release pot full clockwise.
Ratio switch set to 20:1.
Set the meter switch to the GR position.

Note: The meter on the 1176LN will have to be calibrated before this test is done in order to determine the 1 db of limiting point easily.

Test: Apply a 2 kHz, -10 dbu input signal and set an output reference level of 10 dbu with the input and output pots for each channel. Set the attack switch full on and set the 1176LN into 1 db of limiting using the meter on the limiter. Also, set an output reference level with the output pot.

Use the above setup for each of the ratio settings of 20:1, 12:1, and 8:1. The 4:1 ratio setting will need to be set for 3 db of limiting because of the soft knee in the threshold circuit for this ratio. A new output reference level must be made for each new ratio setting.

Increase the input signal 20 db and see that the output level will increase, from the new output reference level that must be set for each ratio, 1 db, 1.66 db, 2.5 db, and 5 db for each ratio setting respectively with a 20% tolerance for each ratio setting. Repeat this setup and test using 20 Hz and 20 kHz input signals. The 20 Hz test will need to be done with the release pot in the full CCW position.
14. Attack and Release Times  

Conditions: Tone Burst generator is needed.
Attack pot full counterclockwise. (Switched off.)
Release pot full clockwise.
Ratio switch set to 20:1.
Set the meter switch to the GR position.

Test: Apply a 2 kHz, -10 dbu, sine wave input signal and set an output reference level of 10 dbu with the input and output pots for each channel. Set the attack full on and adjust the 1176LN for 10 db of limiting. Set an output reference level with the output pot.

Apply a tone burst signal that has the same amplitude as the sine wave setup signal along with a gated output of a duration of 1 msec on and 1.5 seconds off and that also has several cycles of a 20 kHz signal. With an oscilloscope measure the time duration from the peak amplitude on the leading edge of the gated signal to the smallest amplitude (which should be the same as the reference level) to be less than 20 usec when the attack pot is full clockwise and greater than 800 usec when the attack is counterclockwise (but not switched off).

Repeat the sine wave setup above with 10 db of limiting. With an oscilloscope measure the time duration for the signal level to increase to 63% of the final amplitude of the output signal after quickly switching the input signal down to -30 dbu to be no less than 50 msec with the release pot full clockwise and no greater than 1.1 seconds when the release pot is full counterclockwise.

Note: For best resolution of the measurement use the full scale grid of the oscilloscope for the amplitude measurement.

A square wave generator can be used to determine that the attack and release circuit is working by using a 20 Hz input signal with about 10 db of limiting however this method should only be used to compare a known good unit to the device under test but the accuracy of the measurement is not precise.

If this measurement technique is used then it should be observed that when the release pot is full counterclockwise that the amount of difference in the leading edge overshoot, when the attack time is tested through the full range of the pot, will be the greatest and therefore the best means for measuring. When the release time is tested through the full range of the pot there should be a substantial difference in the amplitude and tilt of the square wave.
15. Ratio Switching Envelope

Conditions: 600 Ohm load on the output.
Attack pot full counterclockwise. (Switched off.)
Release pot full clockwise.
Ratio switch set to 20:1.
Set the meter switch to the GR position.

Test: Apply a 2 kHz, -10 dbu input signal and set an output reference level of 10 dbu with the input and output pots. Set the attack switch full on and set the 1176LN into 1 db of limiting using the meters on the limiter. Also, set an output reference level with the output pot.

Set the ratio switch to each of the 12:1, 8:1, and 4:1 settings and see that the output level drops 1 db, 1.5 db, and 2.5 db for each setting respectively. Increase the input signal 20 db and measure the maximum difference in output levels for all ratio settings to be less than 1.5 db.

16. Total Harmonic Distortion

Conditions: Attack pot full clockwise.
Release pot full counterclockwise.
Ratio switch set for 20:1.
600 Ohm load on the output.

Test: For each input signal of 30 Hz, 200 Hz, 2 kHz, and 15 kHz measure the THD at 1 db below clip to be less than .3% if Q1 is removed from the unit. When Q1 is in the 1176LN and it is biased correctly the THD is typically less than .3% when the limiting is switched off. The specification for distortion is from 50 Hz to 15 kHz in limiting at any level up to rated output to be less than .5% THD.

Note: Since the amount of limiting was not specified, and the THD is a function of the amount of limiting, then an amount of 5 db of limiting is what could be used for a typical real world measurement.

Make sure that the signal generator that is used has distortion less than .01% in order to insure an accurate measurement.

17. Threshold of Limiting

Conditions: Input, attack, and release pots full clockwise.
Ratio switch set for 20:1.
Meter switch set for GR.

Test: Apply a -20 dbu, 2 kHz input signal and see that the meter on the 1176LN is showing limiting. Decrease the input signal to the point where the meter is showing only -1 db Vu reading and measure the input signal at this point to be -24 dbu, with ± or - 2 db tolerance. Change the frequency of the input signal to 20 Hz and 20 kHz and see that this threshold remains at the same level.

Repeat this test for the 12:1, 8:1, and 4:1 ratio settings that have -25 dbu, -26 dbu, and -30 dbu thresholds of limiting respectively with ± or - 2 db of tolerance for each setting.

Note: It is important that the meter on the 1176LN has been calibrated and tested before this test is done.
18. Contamination

Conditions: Attack pot full clockwise.
Release pot full clockwise.
Ratio switch set for 20:1.
Meter switch set for GR.

Test: Apply a -10 dbu, 2 kHz input signal and adjust the input pot so that there is 5 db of limiting (use the meter on the 1176LN) then use a hot moist breath for about 5 seconds on Q1 and see that there is no change in level.

Note: The hot breath can be produced by keeping your mouth wide open and gently forcing air from your throat. Also if a change in level does occur after the moist air is applied it is probably due to flux residues from the soldering process and should be cleaned with an isopropyl alcohol and distilled water solution with a 75% alcohol to 25% water mixture.
Calibration instructions for the UREI type 1176LN peak limiter

1. Inject .775 Volts, 1 Khz into the limiter.
2. Turn Trimpot R 81 full CCW.
3. Set input and output level controls of the 1176 LN to “24” (mid rotation).
4. Set attack control full CCW and release control full CW, meter to “GR” and compression ratio to 20:1.
5. Adjust output control for a reading of +11 db (re .775V) as read on an external meter.
6. CAREFULLY adjust R 81 for an output of +10 db.
7. Set attack control full CW.
8. Set output control for 0 db (re .775V) as read on the external meter.
9. Set attack control full CCW.
10. Set input control for +10 db as read on the external meter.
11. Set attack control full CW.
12. Set output control for 0 db as read on the external meter.
13. Repeat from step #9 until a swing of +10 db/0 db is achieved.
14. Set attack full CCW, and meter to “GR”.
15. Adjust the front panel trimpot (accessed through a hole located between the input and output controls) until the internal meter reads zero.
16. Set attack control full CW.
17. Set R 54 until the internal meter reads -10 db.
18. Set attack control full CCW.
19. Adjust the front panel trimpot for zero.
20. Repeat from step #16 until the internal meter reads zero and -10 with the attack control CCW and CW respectively.
21. Set attack control full CW.
22. Set meter switch to “+4” and confirm that the internal 7 meter reads -4 db.
23. Set meter switch to “+8”. The meter should read -8 db.
METER CALIBRATION
1176
WITH DISCRETE COMPONENTS
IN METER CIRCUIT

SETUP:
NO SIGNAL
DISCONNECT R44
CENTER (NULL) POT. R75
VOLTMETER ACROSS R74

CALIBRATION:
(A) ZERO 1176 "GR" METER WITH R71 (THRU FRONT PANEL
ZERO ADJ.
(B) ADJUST "NULL" POT. FOR .00 VOLTS ACROSS R74. REPEAT
A AND B UNTIL BOTH CONDITIONS ARE MET.

(C) ADJUST LIMITER FOR 10DB OF LIMITING, AS INDICATED
IN THE 1176 CALIBRATION INSTRUCTION.

(D) SELECT RESISTOR R44 FOR CORRECT TRACKING, IT MAY
BE NECESSARY TO REPEAT STEPS A, B, C TO FINE TUNE THE
CIRCUIT.
Power Supply +30, +14, -10, 220–115 V

Gain -25 in, +20 out (+2 db) versus 121.25 db

Clipping = +21.25 db

Freq Resp & Dist +23 out 30–20 kHz 1/8 db, ±3 %

CMRR = –40 db

Noise = –60 db, Q' = –55 db, W = –61 db

\[ \frac{1}{Q'} = -10 \text{ in} \quad 1 \text{kHz} \]

\[ Q' = +11 \text{ db output} \]

With R81 turn cw until output drops 1 db (±10 db)

Sw. Attack Pot Q note 10 db limiting (if not adjusted with output pot)

Turn sw. Attack pot off Q output adjusted with input pot

With R85 (hole on F.P) adjust pointer to 0°

Attack Pot Q 90°, note should read -10 db limiting

If not adjusted with R84, check several times

Measure voltage on wiper of R81, should read \(-10 \text{ to } 2 \text{ V} \)

Check +4, ±0.5 db ± 0

Adjust input to 10 db limiting until output to ±15 db

Measure the full voltage AC must be symmetrical

\[ \frac{1}{2} \text{ Vrms } \text{ max}, \text{ Vpp } \text{ max } \text{ very } \pm 1 \text{ %} \]
attack pot \( q_{cw} \)

output adjusted with output pot

with input pot, adjust meter on unit to read \(-1 \) (1db. limiting)

\[
\begin{align*}
12:1 & \quad \text{meter should read} \quad -2.0 \text{ db} \quad (2\text{db}) \\
8:1 & \quad \text{should read} \quad -2.5 \text{ db} \quad \text{within } \frac{1}{2}\text{ db} \quad \text{unit} \\
4:1 & \quad \text{unit} \quad -4.0 \text{ db} \\
\end{align*}
\]

with output pot, decrease output db now should read \( +9 \) db.

increase input 20db, 20:1 = 9.9db

decrease input pot, 12:1 = 9.3db

8:1 = 9.2db

4:1 = 9.5db

10in. = 10 out 5db limiting

12, 500Hz = 10db in 10db limiting 20:1

10db output with R16 adjust dist to minimum

\[
\text{Contamination}
\]

attack, release

18, attack off meter \( +4 \) (meter on unit \( \beta_p \) = 0. Breathe out

\[
\begin{align*}
20:1 & = 10 \text{db} \\
12:1 & = 9 \text{db} \\
8:1 & = 10 \text{db} \\
4:1 & = 11 \text{db}
\end{align*}
\]
The 1176 SA Stereo Adaptor is used to couple two UREI Model 1176 Limiting Amplifiers, to permit equal gain reduction in both channels of a stereo transmission when a peak occurs in either channel. Complete audio isolation is maintained between the two channels. This type of limiting is desirable in stereo FM broadcasting to prevent transmitter over-modulation without shifting the aural placement of point source sounds.

MOUNTING INSTRUCTIONS

The two 1176 Limiters which are to be coupled for stereo operation should preferably be mounted in immediately adjacent locations in a rack or enclosure. If they must be separated, longer coupling cables of a type similar to those furnished with the 1176 SA will have to be provided.

The 1176 SA can be affixed to the rear of the chassis of either 1176 (or to any other adjacent flat, smooth surface) by means of the pressure-sensitive adhesive on the back of the adaptor. A suggested location is in the rear center of the 1176, directly above the words "United Recording Electronics Industries".

A. Remove green protective paper from adhesive by inserting fingernail under one corner. Do not touch adhesive with fingers, as oil will adversely affect its bonding ability.

B. Locate the Adaptor box carefully over the area where it is to be mounted, then press the adaptor firmly to the surface. This must be done accurately, for the position of the 1176 SA cannot be changed after contact without destroying the adhesive coating.

C. Insert one end of each of the two shielded cables into the jacks on the Adaptor, seating the connectors securely. Connect the other ends to the two limiters by inserting the connectors into the jacks marked "1176 SA" on the extreme right rear of the chassis.

D. Follow the calibration procedure outlined in the 1176 Limiting Amplifier instruction manual.

The NEDA 15M mercury cell installed in the 1176 SA at the factory provides a reference bias only, and should last the shelf life of the battery, which is approximately 18 months. We suggest, however, that as part of your preventive maintenance a fresh cell be installed every six to nine months.

To replace the cell, remove the front plate assembly from the 1176 SA by removing the four Phillips screws. Observe correct polarity when inserting the fresh cell. Replace assembly and re-balance the system with reference to the Limiter Instruction Manual.

UNITED RECORDING ELECTRONICS INDUSTRIES
8460 San Fernando Road
Sun Valley, California 91352
BILL OF MATERIALS

<table>
<thead>
<tr>
<th>No.</th>
<th>MFG &amp; PART NO</th>
<th>DESCRIPTION</th>
<th>QTY</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>OLUMITE CLU1041 OR EQU.</td>
<td>POTENTIOMETER 100K-2 LINEAR (R1)</td>
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</tr>
<tr>
<td>1</td>
<td>RESISTOR 150 KΩ, 1/2 W, 5% 82K 5% 1/2 W (R2)</td>
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</tr>
<tr>
<td>1</td>
<td>HOLLAND 111 OR EQU.</td>
<td>CAPACITOR 60 μF, 25 V 100 μF 25 V (C1)</td>
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</tr>
<tr>
<td>1</td>
<td>EVEREADY E-9 OR EQU.</td>
<td>PENLIGHT MERCURY NEDA 15 M (1.4 V) (B)</td>
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</tr>
<tr>
<td>1</td>
<td>KEYSTONE 139 OR EQU.</td>
<td>BATTERY HOLDER (FOR B)</td>
<td>1</td>
</tr>
<tr>
<td>2</td>
<td>SWITCHCRAFT 3601FP</td>
<td>PHONO JACKS</td>
<td>2</td>
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<tr>
<td>1</td>
<td>OLSON ELECTRONICS</td>
<td>PLASTIC BOX W/ALUMINUM COVER (SEE NOTES 1 &amp; 2)</td>
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<td>1</td>
<td>UREI 8-1082G</td>
<td>SHIELD</td>
<td>1</td>
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<tr>
<td>4</td>
<td>MAGNETS 1 x 3/4 x 1/8&quot; DOUBLE STICK TAPE</td>
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<tr>
<td>2</td>
<td>4-40X3/16&quot; BHMS</td>
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<td>1</td>
<td>6-32X7/16&quot; BHMS</td>
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<td>SERIAL NUMBER</td>
<td>&quot;REVISION&quot;</td>
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<tr>
<td>101 - 216</td>
<td>REV A</td>
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<td>217 - 1078</td>
<td>REV B</td>
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<td>1079 - 1238</td>
<td>REV C</td>
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<td>1239 - 2331</td>
<td>REV D</td>
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<td>2322 - 2610</td>
<td>REV E</td>
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<td>2611 - 7052</td>
<td>REV F</td>
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<td>7053 - 7651</td>
<td>REV G</td>
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<tr>
<td>7652 onwards</td>
<td>REV H</td>
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<tr>
<td></td>
<td>REV J</td>
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</tr>
<tr>
<td></td>
<td>REV K</td>
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</tr>
</tbody>
</table>
ORIGINAL TYPE SU 125-216
FET INPUT AMPLIFIER
OR FET GATE couples TO input.

TWO TRANSISTOR INPUT AMPLIFIER
POWER TRANSFORMER MAY BE ONE OR TWO TYPES
PT-5101 OR UA-861A
REVISED FETcoupling TO feedback OR
PREAMP.

BLACK FRONT PANEL
L-N MODULE ON 1176 "B" P.C. BOARD
BOOTSTRAP CONNECTION FOR LOWER 3RD HARMONIC
DISTORTION
LOWER BAND BY 5 dB.
LOWER 6 dB SENSITIVITY BY 5 dB.
LOWER NOISE BY 3 dB.
NO SCHEMATIC EXISTING.

INTEGRATED L-N MODIFICATION ON
P.C. BOARD
CHANGE TO 13-002-7 FET.
POWER TRANSFORMER PT-983.
GROUNDING POWER CORD,
METER LIGHTS.

10 M RESISTANCE ON RATIO SWITCH.
1175-4 POWER TRANSFORMER.
115-230V SWITCH.

OP AMP FOR 6R MOTOR CIRCUIT.
14-1148 OUTPUT TRANSFORMER
DIFFERENT OUTPUT AMP.

BRIDGING INPUT
THRESHOLD LOWER BY 6 dB.
LOWER NOISE WITH INPUT CONTROL "BELOW 1/4".
1C VOLTAGE REGULATOR IN POWER SUPPLY.
output transformer loaded and optimized to operate into load or no-load.

Jumpers for change to European 60 ohm output impedance.

New look front panel:
- Backlit VU meter
- IEC power connector
- Meter terminals removed

12614 output transformer
356 I A format meter driver.
No modification necessary for export output impedance.
9. TOTAL UNUSED OPENINGS IN TRANSFORMER CASE SHALL NOT EXCEED .25 X 30 INCHES

8. VARNISH XMPR BEFORE INSTALLING CHANNEL OR END BELLS

7. TRANSFORMER MUST WITHSTAND 900 V H.P. BETWEEN ANY LEADS NOT INTERNALLY CONNECTED OR BETWEEN ANY LEAD AND CASE

6. INTERNAL HEAT RISE AT 160 MA LOAD CURRENT WITH 120 VOLT 50 Hz LINE MUST NOT EXCEED 40°C

5. WHEN RL IS OPEN E MUST NOT EXCEED 48 VOLS D.C. WHEN RL = 150 K E MUST NOT BE LESS THAN 35 V.D.C.

4. LEADS MUST BE FIRMLY ANCHORED CONSISTENT WITH GOOD TRANSFORMER DESIGN PRACTICE AND MUST BE ABLE TO WITHSTAND 10 LB. PULL

3. LEADS SHALL BE #22 STRANDED R.V.C. 600V INSULATION

2. TRANSFORMER MUST BE FREE OF EXCESS WAX AND VARNISH AND PRESENT A CLEAN EXTERNAL APPEARANCE.

1. TRANSFORMER INCLUDES END BELLS WITH LEADS EXITING AS SHOWN

NOTES:

11/754
1. Indicates Leads Shall Not Be Cut Off At Board
2. Adding #22 Gauge Buss Wire With 1" Long
3. 150 Ohms Resistor, R85A
N-Channel General Purpose Amplifier

This device is a low level audio amplifier and switching transistors, and can be used for analog switching applications. Sourced from Process 55.

Absolute Maximum Ratings* TA = 25°C unless otherwise noted

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Value</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>V_DG</td>
<td>Drain-Gate Voltage</td>
<td>25</td>
<td>V</td>
</tr>
<tr>
<td>V_GS</td>
<td>Gate-Source Voltage</td>
<td>-25</td>
<td>V</td>
</tr>
<tr>
<td>I_GF</td>
<td>Forward Gate Current</td>
<td>10</td>
<td>mA</td>
</tr>
<tr>
<td>T_J,  T_stg</td>
<td>Operating and Storage Junction Temperature Range</td>
<td>-55 to +150</td>
<td>°C</td>
</tr>
</tbody>
</table>

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:
1) These ratings are based on a maximum junction temperature of 150 degrees C.
2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics TA = 25°C unless otherwise noted

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Characteristic</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>P_D</td>
<td>Total Device Dissipation</td>
<td>625</td>
<td>mW</td>
</tr>
<tr>
<td></td>
<td>Derate above 25°C</td>
<td>5.0</td>
<td>mW/°C</td>
</tr>
<tr>
<td>R_THJC</td>
<td>Thermal Resistance, Junction to Case</td>
<td>125</td>
<td>°C/W</td>
</tr>
<tr>
<td>R_THJA</td>
<td>Thermal Resistance, Junction to Ambient</td>
<td>357</td>
<td>°C/W</td>
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<td></td>
<td>*Device mounted on FR-4 PCB 1.6&quot; X 1.6&quot; X 0.06. *</td>
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</tr>
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</table>

©1997 Fairchild Semiconductor Corporation
### Electrical Characteristics

*TA = 25°C unless otherwise noted*

#### OFF CHARACTERISTICS

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{BR\text{GSS}}$</td>
<td>Gate-Source Breakdown Voltage</td>
<td>$I_G = 10 \mu A, V_{DS} = 0$, $V_{GS} = -15 V$</td>
<td>- 25</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$I_{GSS}$</td>
<td>Gate Reverse Current</td>
<td>$V_{GS} = -15 V$, $V_{DS} = 0$, $T_A = 100°F$</td>
<td>- 1.0</td>
<td>- 200</td>
<td></td>
<td>nA</td>
</tr>
<tr>
<td>$V_{GS\text{off}}$</td>
<td>Gate-Source Cutoff Voltage</td>
<td>$V_{DS} = 15 V, I_D = 10 nA$</td>
<td>5457</td>
<td>- 0.5</td>
<td>- 6.0</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>5458</td>
<td>- 1.0</td>
<td>- 7.0</td>
<td>V</td>
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<td></td>
<td></td>
<td></td>
<td>5459</td>
<td>- 2.0</td>
<td>- 8.0</td>
<td>V</td>
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<tr>
<td>$V_G$</td>
<td>Gate-Source Voltage</td>
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<td>- 2.5</td>
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<td>V</td>
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<tr>
<td></td>
<td></td>
<td></td>
<td>5458</td>
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<td>5459</td>
<td>- 4.5</td>
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<td>V</td>
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#### ON CHARACTERISTICS

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
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<th>Min</th>
<th>Typ</th>
<th>Max</th>
<th>Units</th>
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<tbody>
<tr>
<td>$I_{DSS}$</td>
<td>Zero-Gate Voltage Drain Current*</td>
<td>$V_{DS} = 15 V, V_{GS} = 0$</td>
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<td>1.0</td>
<td>3.0</td>
<td>5.0 mA</td>
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<td>5458</td>
<td>2.0</td>
<td>6.0</td>
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<td>5459</td>
<td>4.0</td>
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#### SMALL SIGNAL CHARACTERISTICS

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<th>Parameter</th>
<th>Test Conditions</th>
<th>1000</th>
<th>1500</th>
<th>2000</th>
<th>5000</th>
<th>5500</th>
<th>6000</th>
<th>Units</th>
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<tbody>
<tr>
<td>$g_s$</td>
<td>Forward Transfer Conductance*</td>
<td>$V_{DS} = 15 V, V_{GS} = 0, f = 1.0 kHz$</td>
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<td></td>
<td></td>
<td>(\mu)hos</td>
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<td>5458</td>
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<td></td>
<td></td>
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<td>(\mu)hos</td>
</tr>
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<td>5459</td>
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<td></td>
<td>(\mu)hos</td>
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<td>$g_{os}$</td>
<td>Output Conductance*</td>
<td>$V_{DS} = 15 V, V_{GS} = 0, f = 1.0 kHz$</td>
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<td>500</td>
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<td></td>
<td></td>
<td>(\mu)hos</td>
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<td>$C_{iss}$</td>
<td>Input Capacitance</td>
<td>$V_{DS} = 15 V, V_{GS} = 0, f = 1.0 MHz$</td>
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<td>4.5</td>
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<td>pF</td>
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<td>$C_{rss}$</td>
<td>Reverse Transfer Capacitance</td>
<td>$V_{DS} = 15 V, V_{GS} = 0, f = 1.0 MHz$</td>
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<td>1.5</td>
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<td>pF</td>
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<tr>
<td>NF</td>
<td>Noise Figure</td>
<td>$V_{DS} = 15 V, V_{GS} = 0, f = 1.0 kHz$, $R_G = 1.0$ megohm, BW = 1.0 Hz</td>
<td>5457</td>
<td>3.0</td>
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<td>dB</td>
</tr>
</tbody>
</table>

* Pulse Test: Pulse Width ≤ 300 ms, Duty Cycle ≤ 2%

### Typical Characteristics

**Transfer Characteristics**

![Transfer Characteristics](image1)

- $V_{GS} = 15 V$
- $V_{GS\text{OFF}} = -2.6 V$
- $I_D = 10 \text{ mA}$
- $I_D = 8 \text{ mA}$
- $I_D = 6 \text{ mA}$
- $I_D = 4 \text{ mA}$
- $I_D = 2 \text{ mA}$
- $I_D = 0 \text{ mA}$
- $V_{GS} = -2 V$
- $V_{GS} = -4 V$
- $V_{GS} = -6 V$
- $V_{GS} = -8 V$
- $V_{GS} = -10 V$

![Transfer Characteristics](image2)

- $V_{GS} = 15 V$
- $V_{GS\text{OFF}} = -3.7 V$
- $I_D = 20 \text{ mA}$
- $I_D = 16 \text{ mA}$
- $I_D = 12 \text{ mA}$
- $I_D = 8 \text{ mA}$
- $I_D = 4 \text{ mA}$
- $I_D = 0 \text{ mA}$
- $V_{GS} = -2 V$
- $V_{GS} = -4 V$
- $V_{GS} = -6 V$
- $V_{GS} = -8 V$
- $V_{GS} = -10 V$
N-Channel General Purpose Amplifier

Typical Characteristics (continued)

Transfer Characteristics

Output Conductance vs. Drain Current

Parameter Interaction

Common Drain-Source

Transconductance vs. Drain Current

Transistor Characteristics

Common Drain-Source

Typical Characteristics

Transfer Characteristics

Output Conductance vs. Drain Current

Parameter Interaction

Common Drain-Source

Transconductance vs. Drain Current

Typical Characteristics

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Output Conductance vs. Drain Current

Parameter Interaction

Common Drain-Source

Transconductance vs. Drain Current
N-Channel General Purpose Amplifier
(continued)

Typical Characteristics (continued)

- **Channel Resistance vs. Temperature**
  - Graph showing channel resistance vs. temperature for different voltages.
  - $V_{GS} = 100 \text{ mV}$, $V_{GS} = 0 \text{ V}$.
  - $T_A = \text{Ambient Temperature (°C)}$.

- **Leakage Current vs. Voltage**
  - Graph showing leakage current vs. voltage for different temperatures.
  - $T_A = 25 \text{ °C}$, $T_A = 85 \text{ °C}$.
  - $V_{DG} = \text{DRAIN-GATE VOLTAGE (V)}$.

- **Noise Voltage vs. Frequency**
  - Graph showing noise voltage vs. frequency for different voltages.
  - $T_A = 25 \text{ °C}$, $V_{DS} = 15 \text{ V}$.
  - $f = \text{FREQUENCY (kHz)}$.

- **Capacitance vs. Voltage**
  - Graph showing capacitance vs. voltage for different voltages.
  - $V_{GS} = \text{GATE-SOURCE VOLTAGE (V)}$.
  - $C_{gs}$, $C_{ss}$.

- **Power Dissipation vs Ambient Temperature**
  - Graph showing power dissipation vs temperature for different packages.
  - TO-92, SOT-23.
  - $P_o = \text{POWER DISSIPATION (mW)}$.
  - $T_A = \text{TEMPERATURE (°C)}$. 

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PRODUCT STATUS DEFINITIONS

Definition of Terms

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<th>Definition</th>
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